| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|---|----------|---|
| 1 | 68 | semiconductor and (insulat\$ adj gate) and | USPAT | 2004/07/09 09:59 |
| | | trench and spacer and source and drain and | | |
| | | conduct\$ and (pillar or column) | 1 | |
| 2 | 44 | semiconductor and (insulat\$ adj gate) and | US-PGPUB | 2004/07/09 10:00 |
| | | trench and spacer and source and drain and | | |
| _ | | conduct\$ and (pillar or column) | | 1 / / |
| 3 | 0 | semiconductor and (insulat\$ adj gate) and | EPO | 2004/07/09 10:00 |
| | | trench and spacer and source and drain and | | |
| | | conduct\$ and (pillar or column) | JPO | 2004/07/09 10:00 |
| 4 | 0 | semiconductor and (insulat\$ adj gate) and trench and spacer and source and drain and | JPO | 2004/07/09 10:00 |
| | | conduct\$ and (pillar or column) | | |
| 5 | 0 | semiconductor and (insulat\$ adj gate) and | DERWENT | 2004/07/09 10:00 |
| | U | trench and spacer and source and drain and | DEKWENT | 2004/07/03 10:00 |
| | | conduct\$ and (pillar or column) | | |
| 6 | 0 | semiconductor and (insulat\$ adj gate) and | IBM TDB | 2004/07/09 10:01 |
| | _ | trench and spacer and source and drain and | | , |
| | | conduct\$ and (pillar or column) | | |
| 7 | 0 | semiconductor and (insulat\$ adj gate) and | IBM TDB | 2004/07/09 10:01 |
|] | | trench and spacer | _ | |
| 8 | 9 | semiconductor and (insulat\$ adj gate) and | DERWENT | 2004/07/09 10:01 |
| • | | trench and spacer | | |
| 9 | 0 | semiconductor and (insulat\$ adj gate) and | JPO | 2004/07/09 10:01 |
| | | trench and spacer | | 1 |
| 10 | 0 | semiconductor and (insulat\$ adj gate) and | EPO | 2004/07/09 10:01 |
| | | trench and spacer | | |